

S/N 09/944,993

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Anthony Moschopoulos	Examiner:	Woo Choi
Serial No.:	09/944,993	Group Art Unit:	2189
Filed:	August 30, 2001	Docket:	703.155US1
Customer No.:	73115	Confirmation No.:	1856
Title:	BIT INVERSION IN MEMORY DEVICES		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants hereby authorize the Commissioner to charge the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p), to Deposit Account No. 19-0743. Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

It is believed that no fee or statement is required with the Information Disclosure Statement. However, if a final action under §1.113, a notice of allowance under §1.311, or an action that otherwise closes prosecution in the application has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

Applicant brings to the attention of the Examiner the following Office Actions and responses associated with one or both of the inventors of the instant application and/or the assignee of the instant application. This material is available and accessible at the United States Patent and Trademark Office.

BIT INVERSION IN MEMORY DEVICES

Application Serial No. 11/460,371 (Atty. Ref. 703.155US2), Non Final Office Action mailed 04-20-2007, 9 pgs
Application Serial No. 11/460,371 (Atty. Ref. 703.155US2), Response filed 07-24-2007 to Non Final Office Action mailed 04-20-2007, 9 pgs
Application Serial No. 11/460,371 (Atty. Ref. 703.155US2), Final Office Action mailed 10/19/2007, 12 pgs
Application Serial No. 11/460,371 (Atty Ref 703.155US2), Response filed 1-22-08 to Final Office Action mailed 10-19-07, 10 pgs

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Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications, and Non-Published Applications identifiable by USPTO Serial Number, are no longer required to be provided to the Office. Notification of this change to this effect was provided in the United States Patent and Trademark Office OG Notice dated October 19, 2004. Thus, Applicant has not included copies of any US Patents or US Patent Applications identifiable by serial number that may be cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

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Date 04/23/08 By Joseph P. Mehrle
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JPM:jld

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web and is addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 30th day of April, 2008.

Name

Signature